

FIG. 1
PRIOR ART

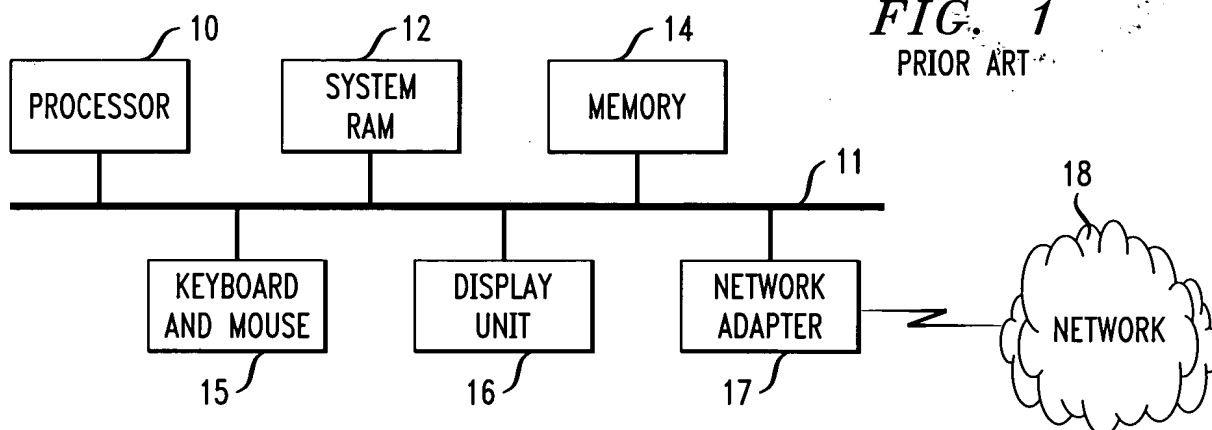


FIG. 2

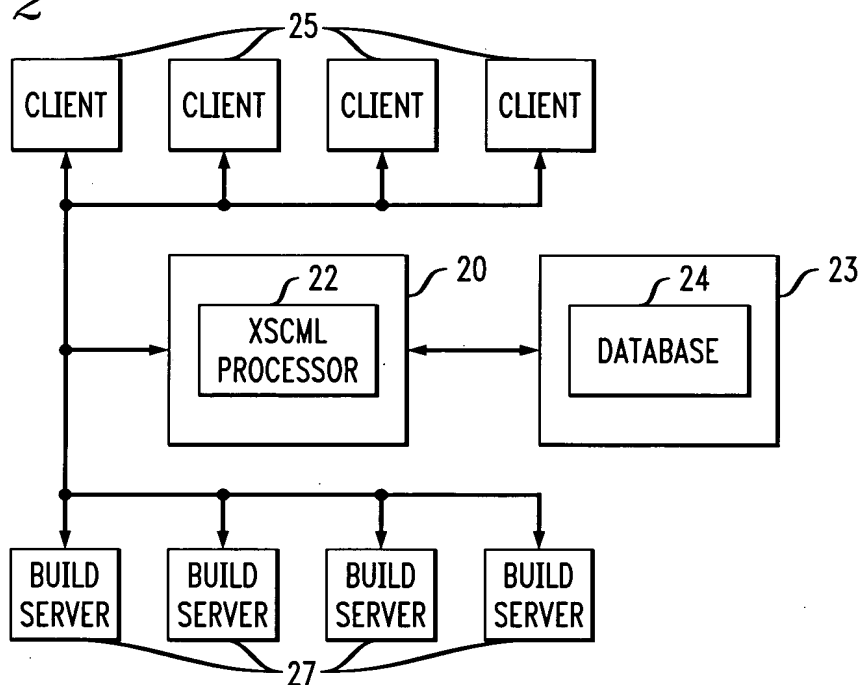


FIG. 3

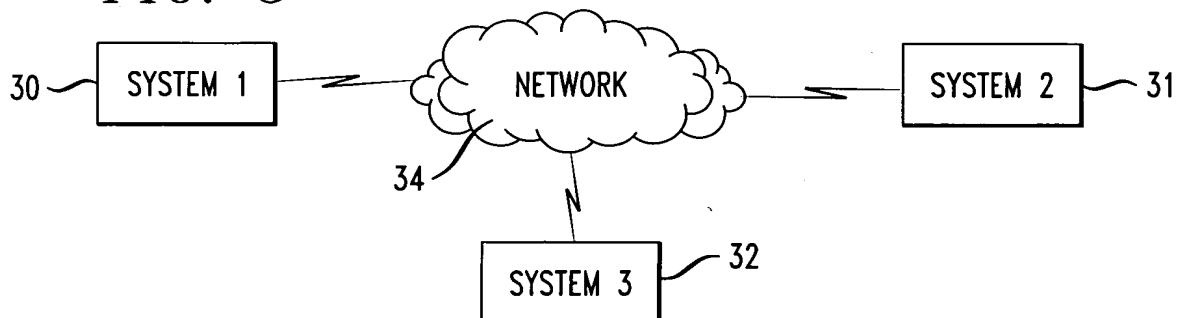


FIG. 4A

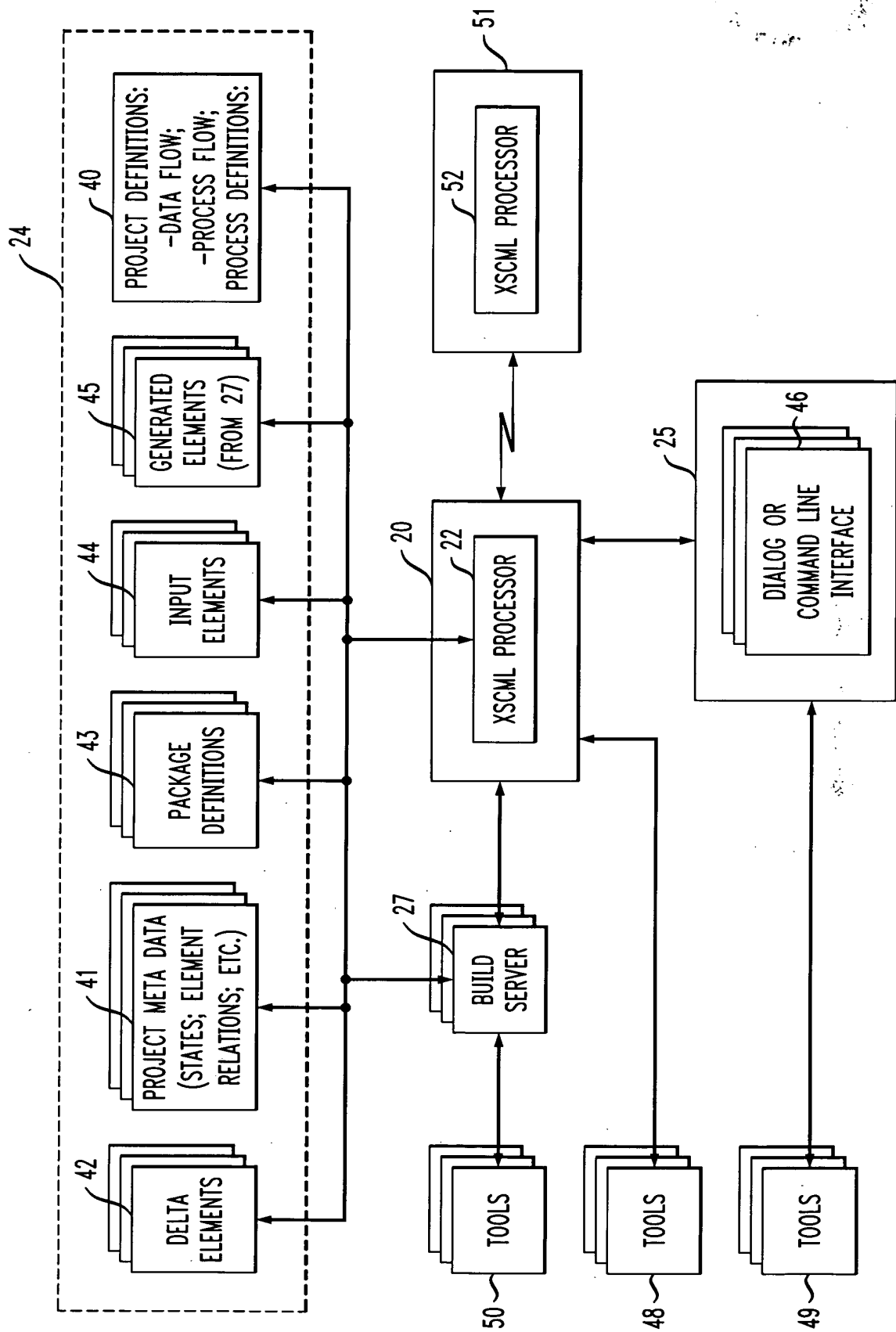


FIG. 4B

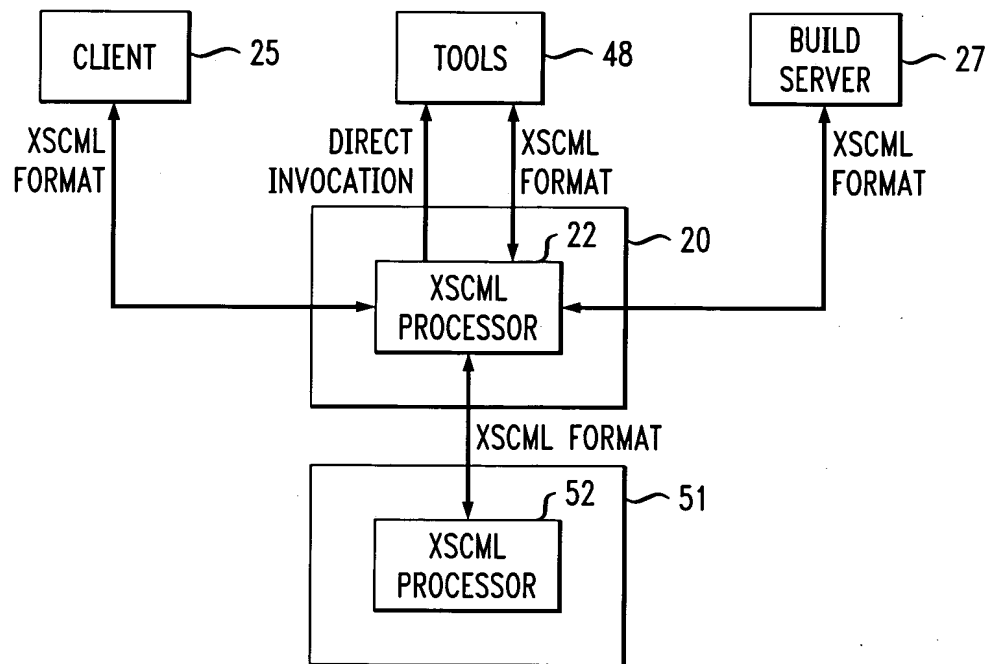


FIG. 5

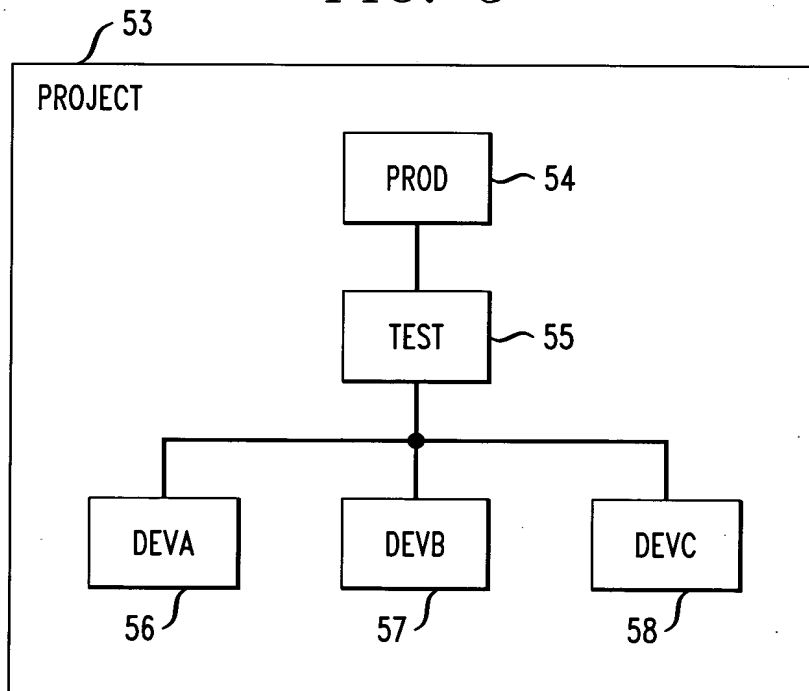
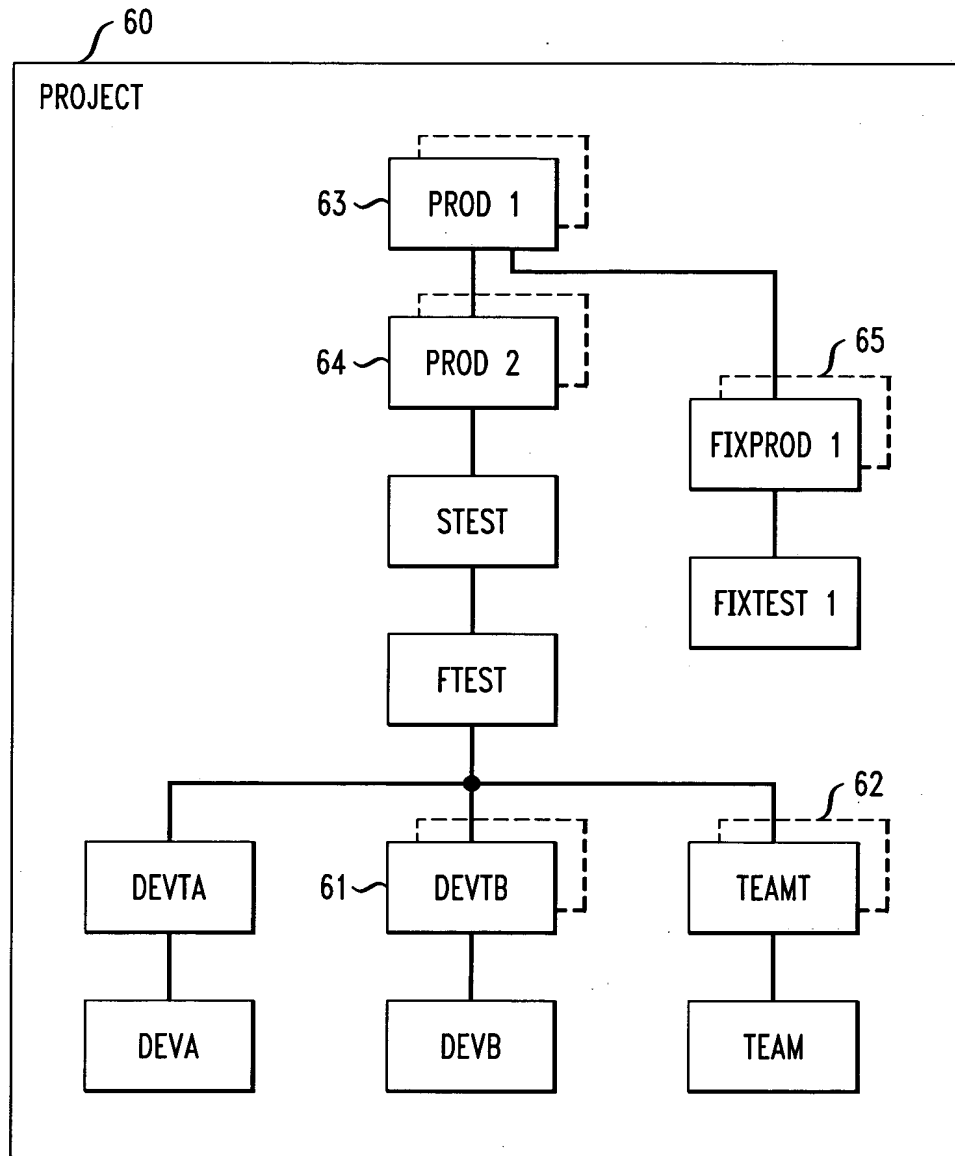


FIG. 6



The flowchart illustrates the system architecture for the XSCML Processor. At the top, a box labeled 'PROJECT META DATA' (41) and a box labeled 'PROJECT DEFINITIONS' (45) both feed into the 'XSCML PROCESSOR' (22). The 'XSCML PROCESSOR' (22) is a central component that interacts with three main functional blocks: 'VERIFY' (77), 'SIGN OFF' (75), and 'JUSTIFY' (74). These three blocks are grouped together in a larger container labeled '73'. Below this container, there are two 'DEVA' blocks. The 'VERIFY' block (77) is connected to the left 'DEVA' block. The 'SIGN OFF' block (75) is connected to the left 'DEVA' block and the right 'DEVA' block. The 'JUSTIFY' block (74) is connected to the right 'DEVA' block. Additionally, there is a block labeled '78' connected to the left 'DEVA' block. The 'XSCML PROCESSOR' (22) also feeds into a block labeled '76', which is connected to the right 'DEVA' block. The 'XSCML PROCESSOR' (22) also feeds into a block labeled '72', which is connected to the right 'DEVA' block. The 'XSCML PROCESSOR' (22) also feeds into a block labeled '71', which is connected to the right 'DEVA' block. The 'XSCML PROCESSOR' (22) also feeds into a block labeled '70', which is connected to the right 'DEVA' block. The 'XSCML PROCESSOR' (22) also feeds into a block labeled '71', which is connected to the right 'DEVA' block. The 'XSCML PROCESSOR' (22) also feeds into a block labeled '70', which is connected to the right 'DEVA' block.

FIG. 8

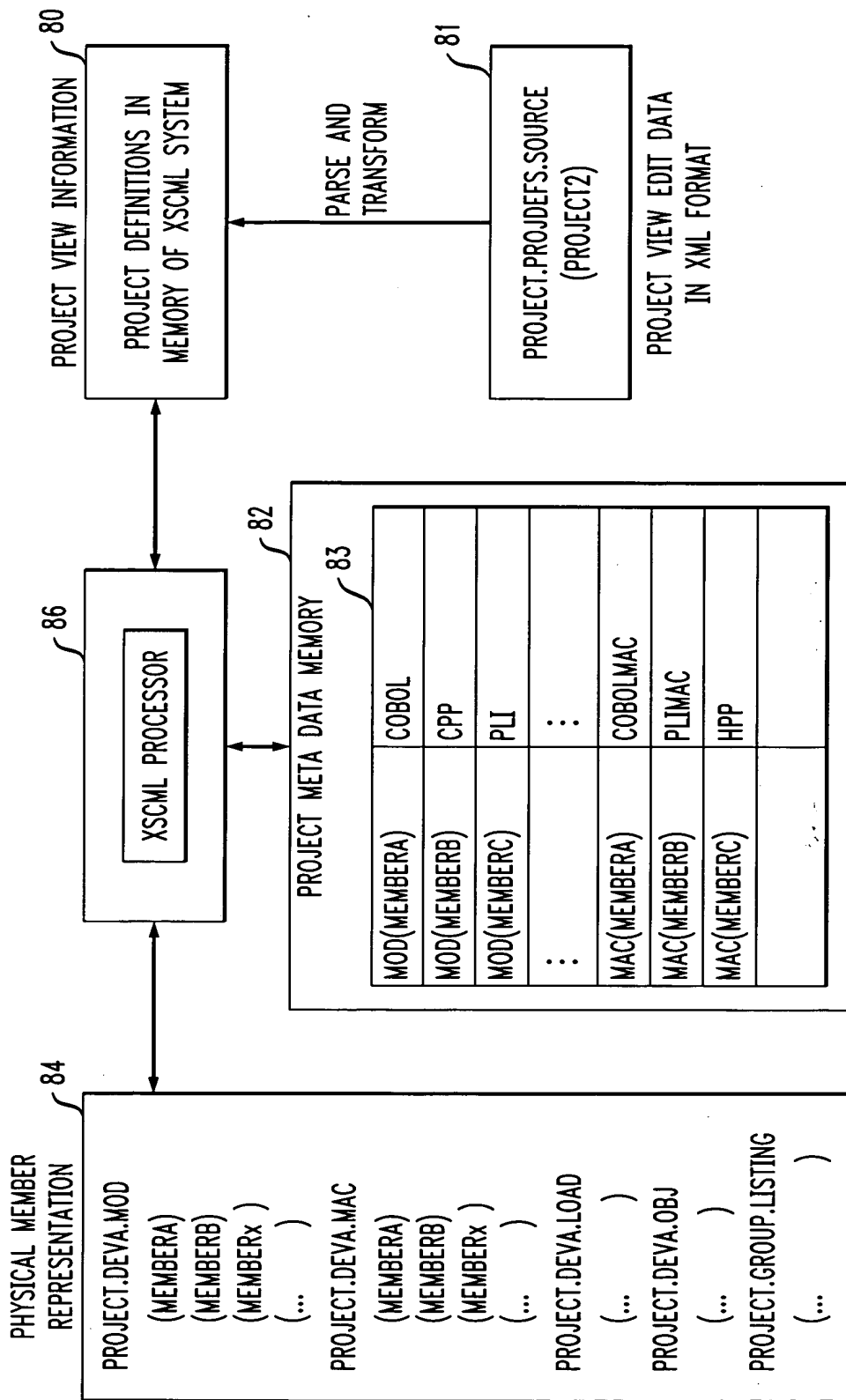


FIG. 9

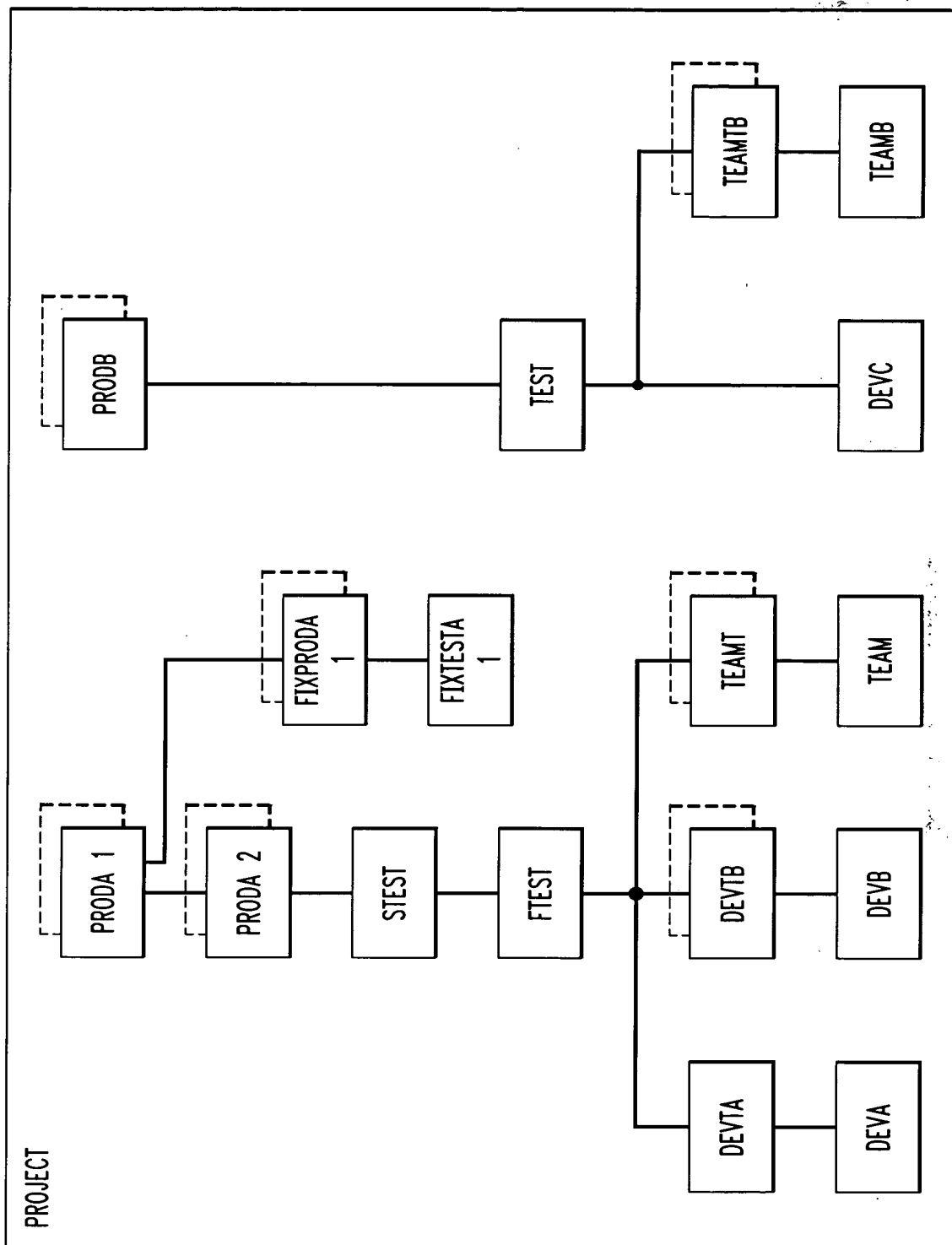


FIG. 10

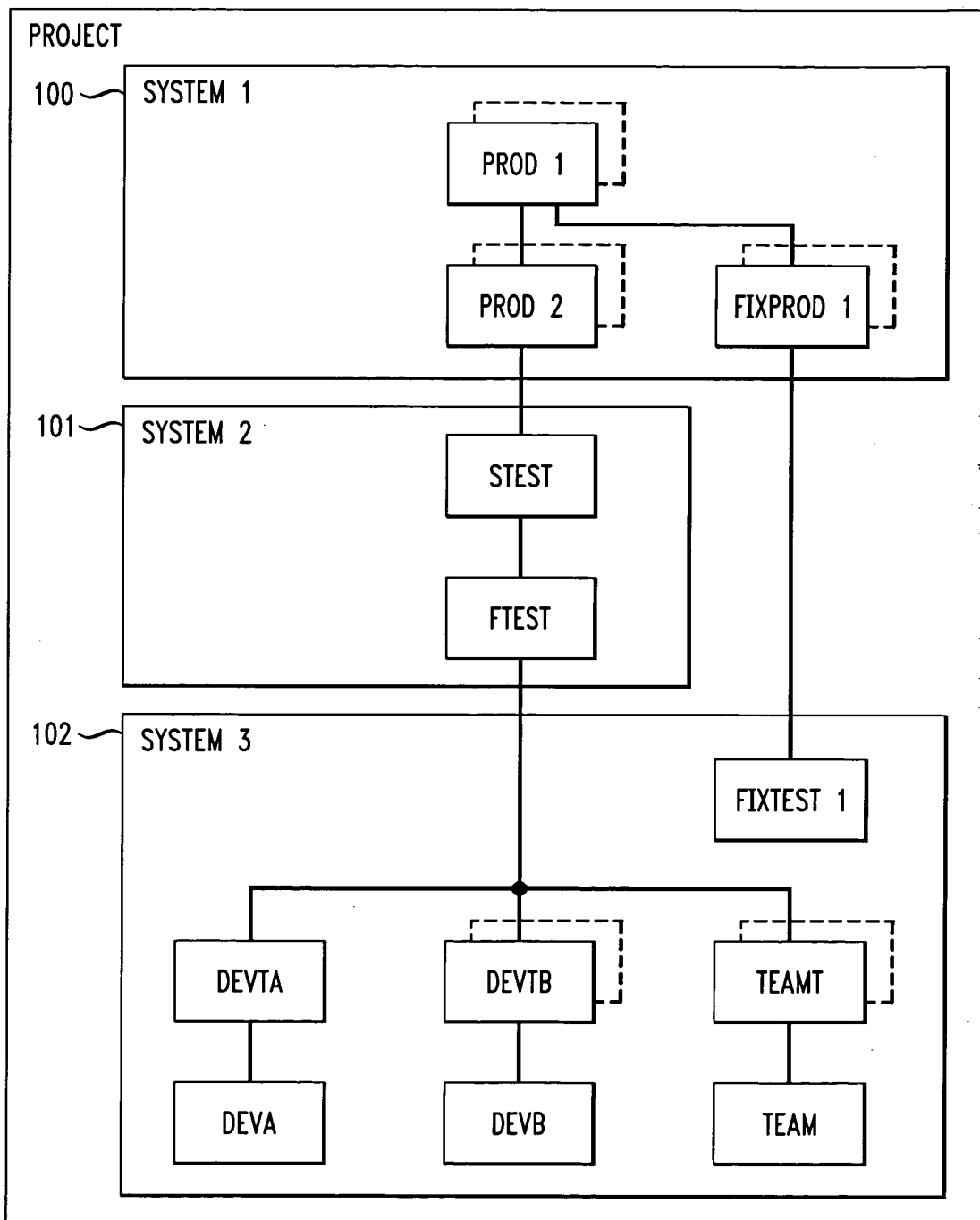


FIG. 11

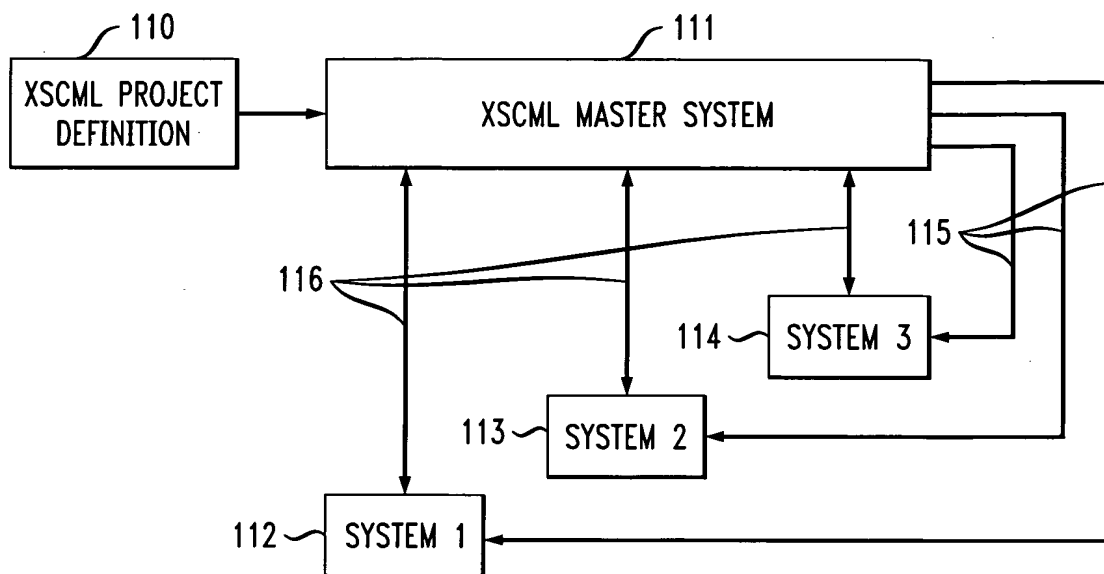


FIG. 12

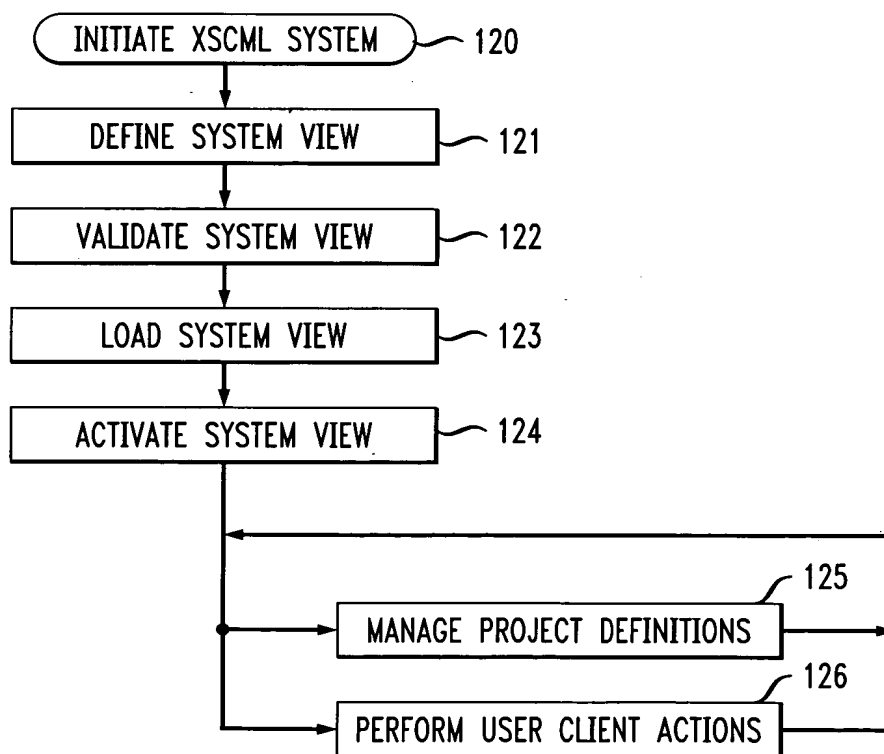


FIG. 13

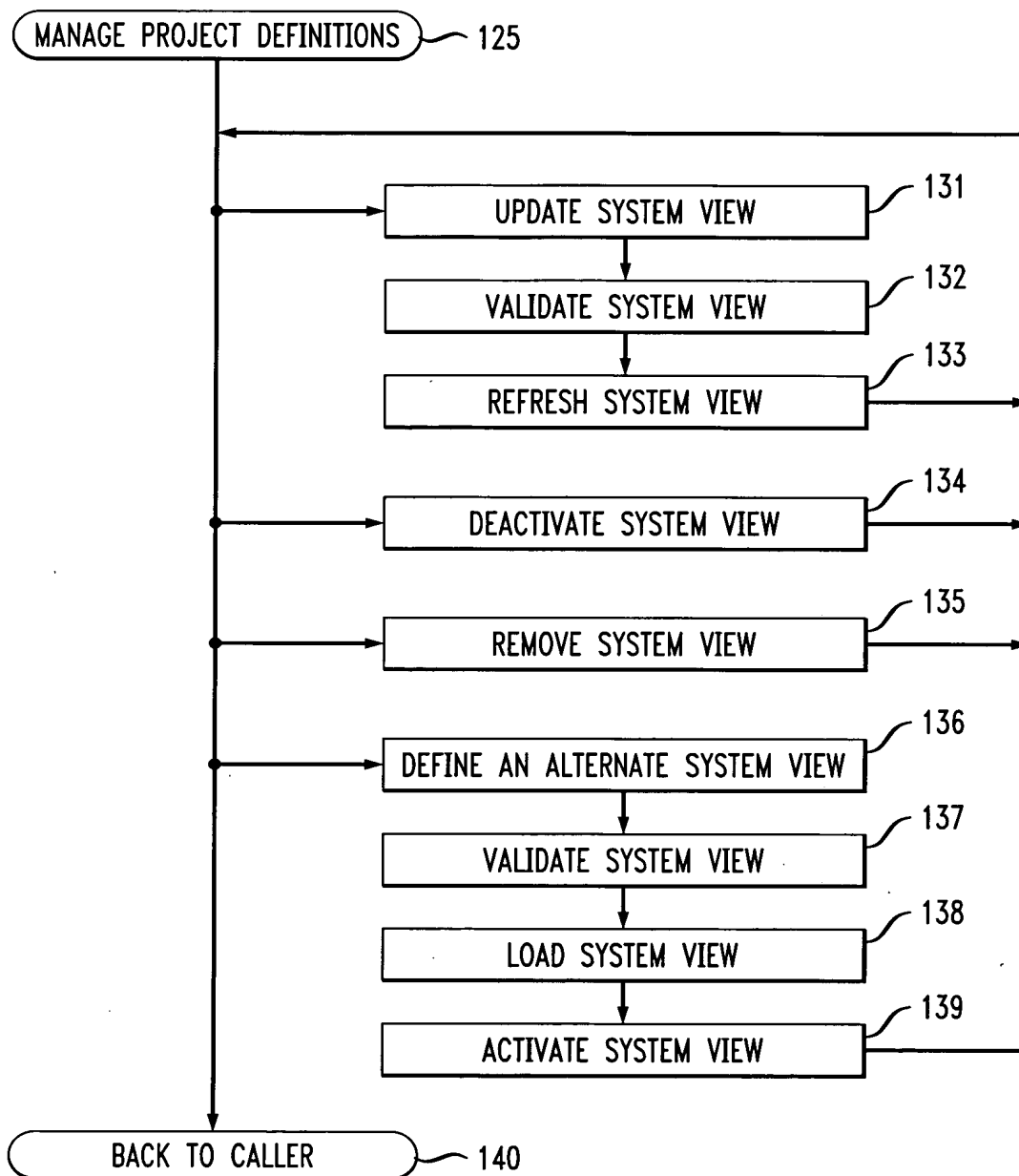


FIG. 14

